

EXPEDITED PROCEDURE – EXAMINING GROUP 2111

S/N 10/814,106

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Paul A. Jolly et al.	Examiner:	Matthew D. Spittle
Serial No.:	10/814,106	Group Art Unit:	2111
Filed:	March 31, 2004	Docket No.:	884.C12US1
Title:	CIRCUIT AND METHOD FOR TRANSFERRING LOW FREQUENCY SIGNALS VIA HIGH FREQUENCY INTERFACE		
Assignee:	Intel Corporation	Customer Number:	21186

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the Final Office Action mailed November 27, 2006, please amend the application as follows: